

FIG. 1

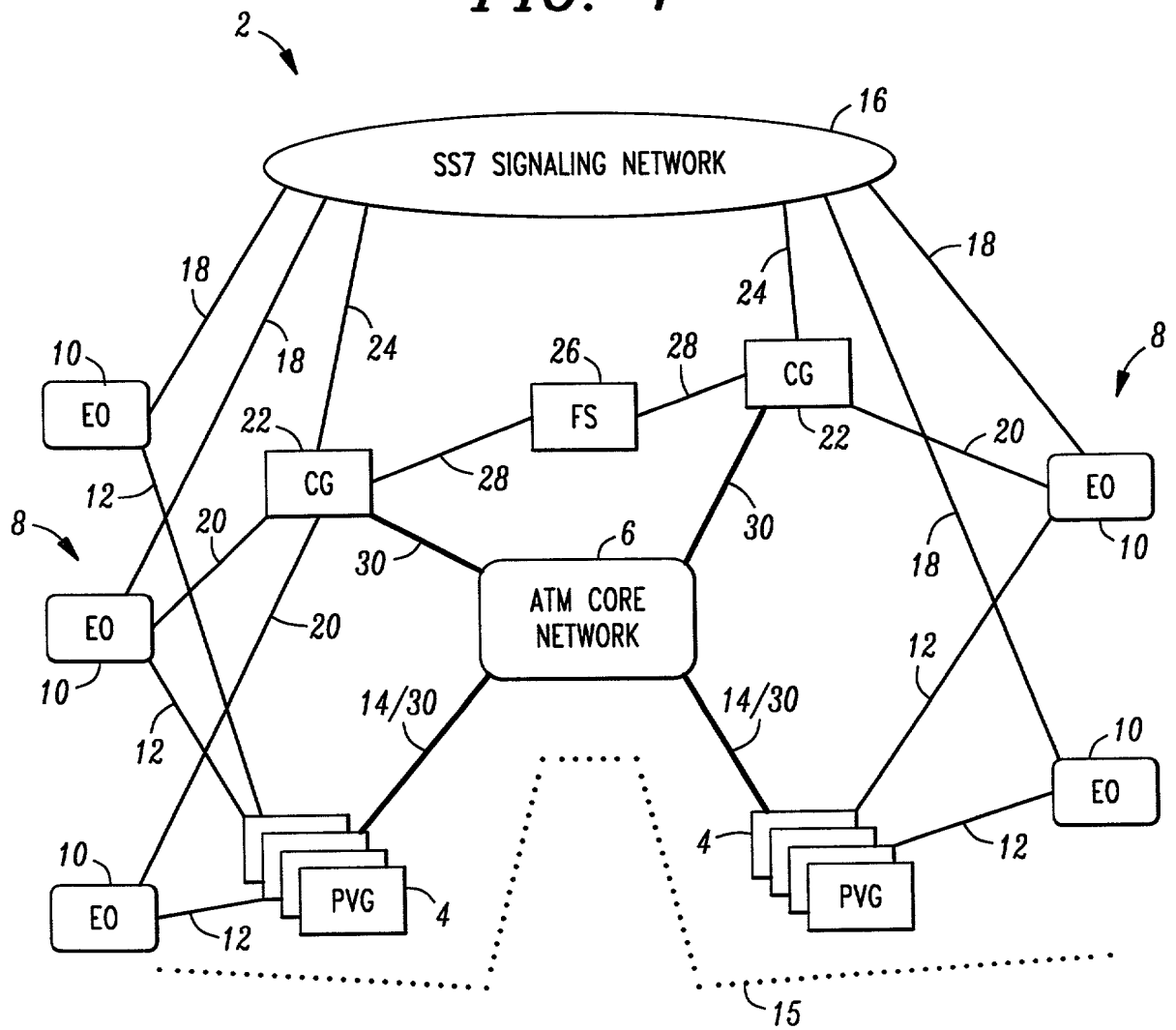


FIG. 2

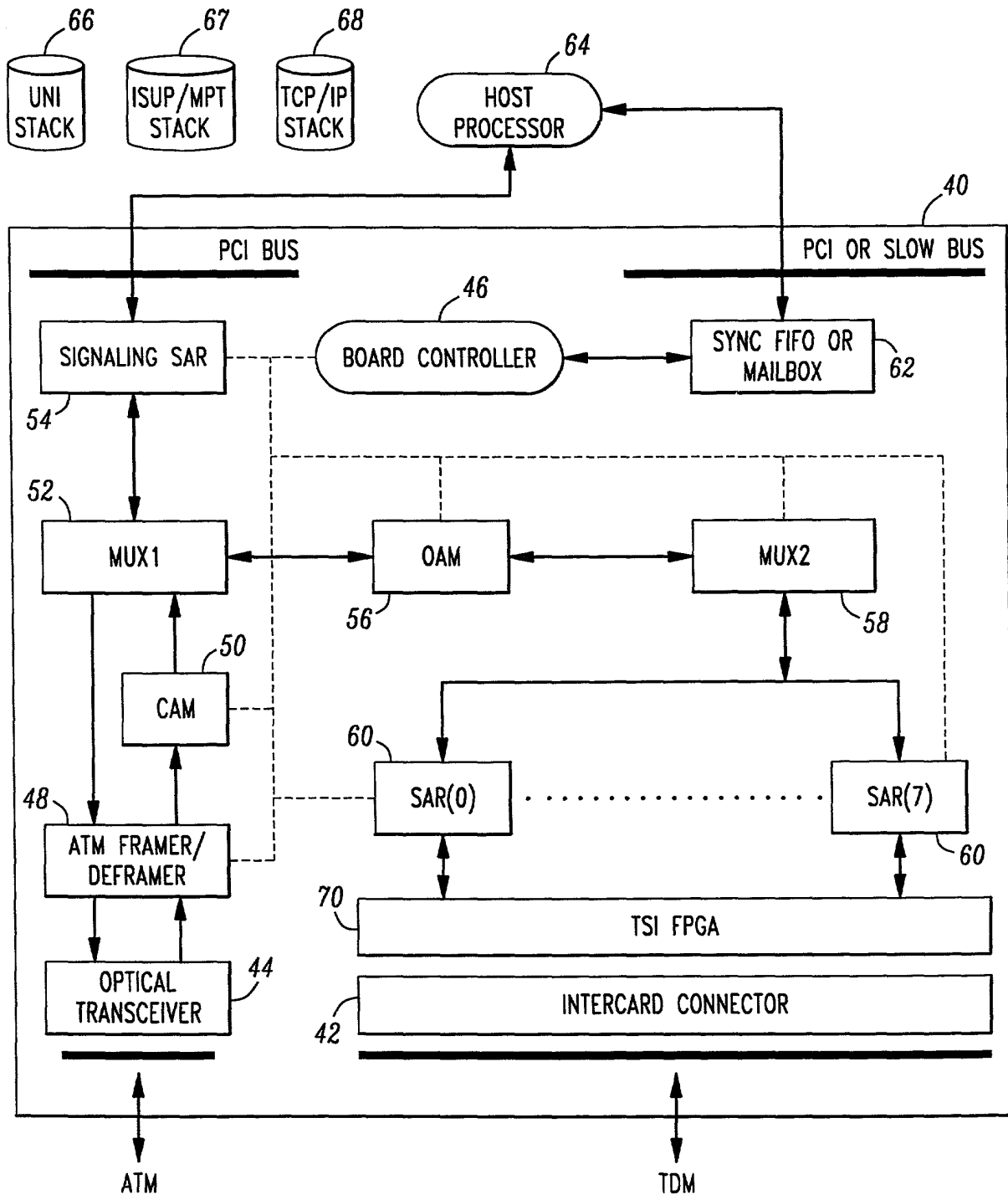


FIG. 3

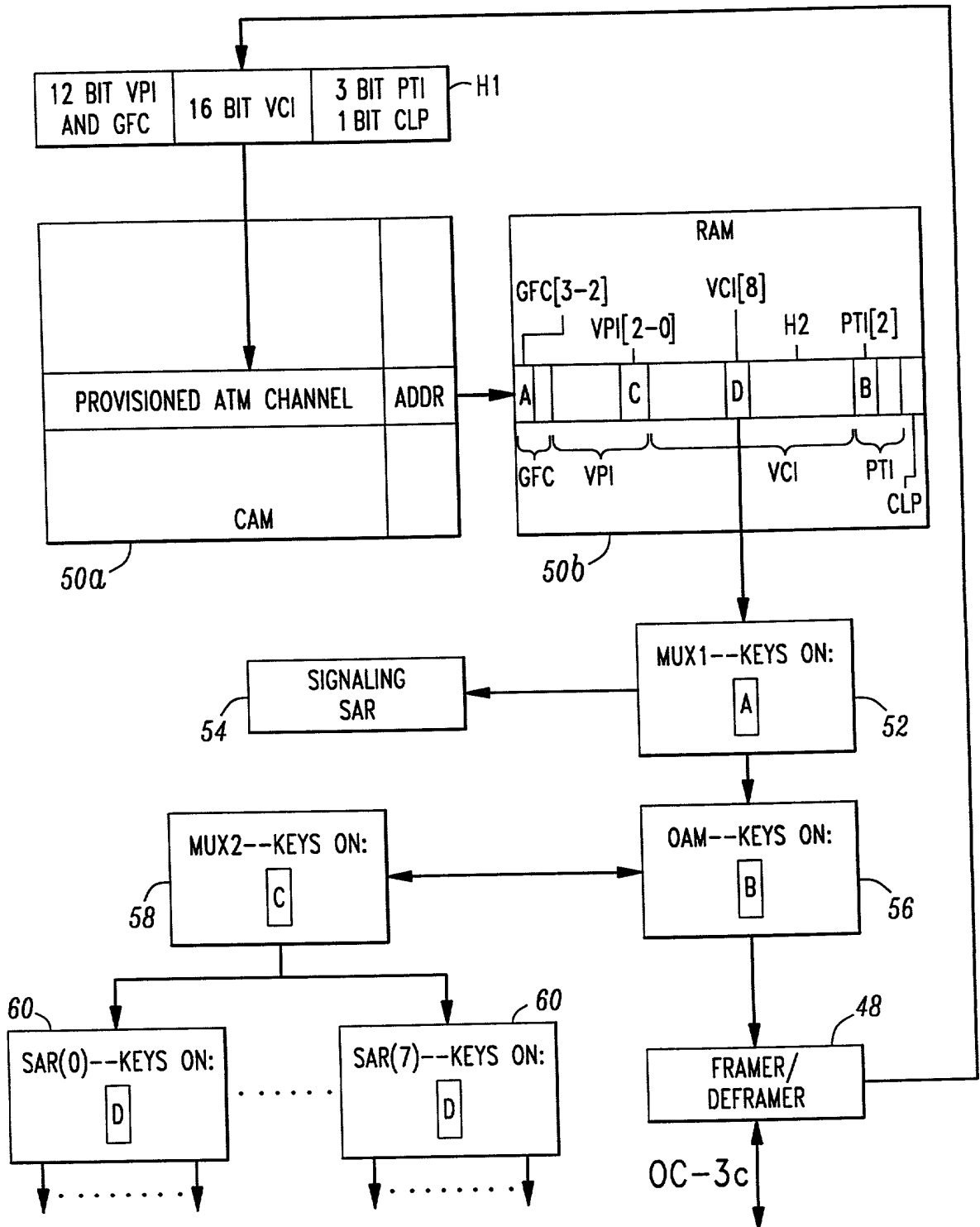


FIG. 4

HEADER FIELD	GFC 4-BIT	VPI 8-BIT	VCI 16-BIT			PTI 3-BIT	CLP	MUX1	OAM	MUX2	SAR
BIT FIELD POSITION	3-2 1-0 (A)	7-3 2-0 (C)	15-8 9 (D)	7-0	2 1-0 (B)			KEY ON GFC[3-2]	KEY ON PTI[2]	KEY ON VPI[2-0]	KEY ON VCI[8]
AAL1 USER CELLS	0 X	ANY X	X 1	ANY	0 X		X	1-T0-4 DEMUX TO OAM	USER CELLS 0 PASS-THROUGH	1-T0-8 DEMUX	256 DS0 CHANNELS
SEG/END F5 OAM CELLS	0 X	=USER X	X 1	SAME AS =USER	1 X		X	SAME AS ABOVE	OAM F5 CELLS ARE TERMINATED	N/A	N/A
SEG/END F4 OAM CELLS	0 X	ANY X	X 0	SEG=3, END-T0-END=4	1 X		X	SAME AS ABOVE	OAM VCI=3 OR 4 ARE TERMINATED	N/A	N/A
SIGNALING HAIRPIN	0 X	ANY X	X 0	ANY	0 X		X	SAME AS ABOVE	PASS THROUGH	1-T0-8 DEMUX	OAM QUEUE
UNHANDLED F5 OAM CELLS	0 X	=USER X	0 1	SAME AS =USER	0 X		X	SAME AS ABOVE	PASS THROUGH	1-T0-8 DEMUX	OAM QUEUE
UNHANDLED F4 OAM CELLS	0 X	ANY X	X 0	SEG=3, END-T0-END=4	0 X		X	SAME AS ABOVE	PASS THROUGH	1-T0-8 DEMUX	OAM QUEUE
AAL5 DATA CELLS	1 ANY	ANY ANY	ANY A	ANY	0 X		X	1-T0-4 DEMUX TO SIGNALING SAR	N/A	N/A	N/A
AAL5 F5 OAM CELLS	1	SAME AS AAL5 DATA CELLS				1 X	X	SAME AS ABOVE	N/A	N/A	N/A
AAL5 F4 OAM CELLS	1 ANY	ANY ANY	ANY 0	SEG=3, END-T0-END=4	X X		X	SAME AS ABOVE	N/A	N/A	N/A

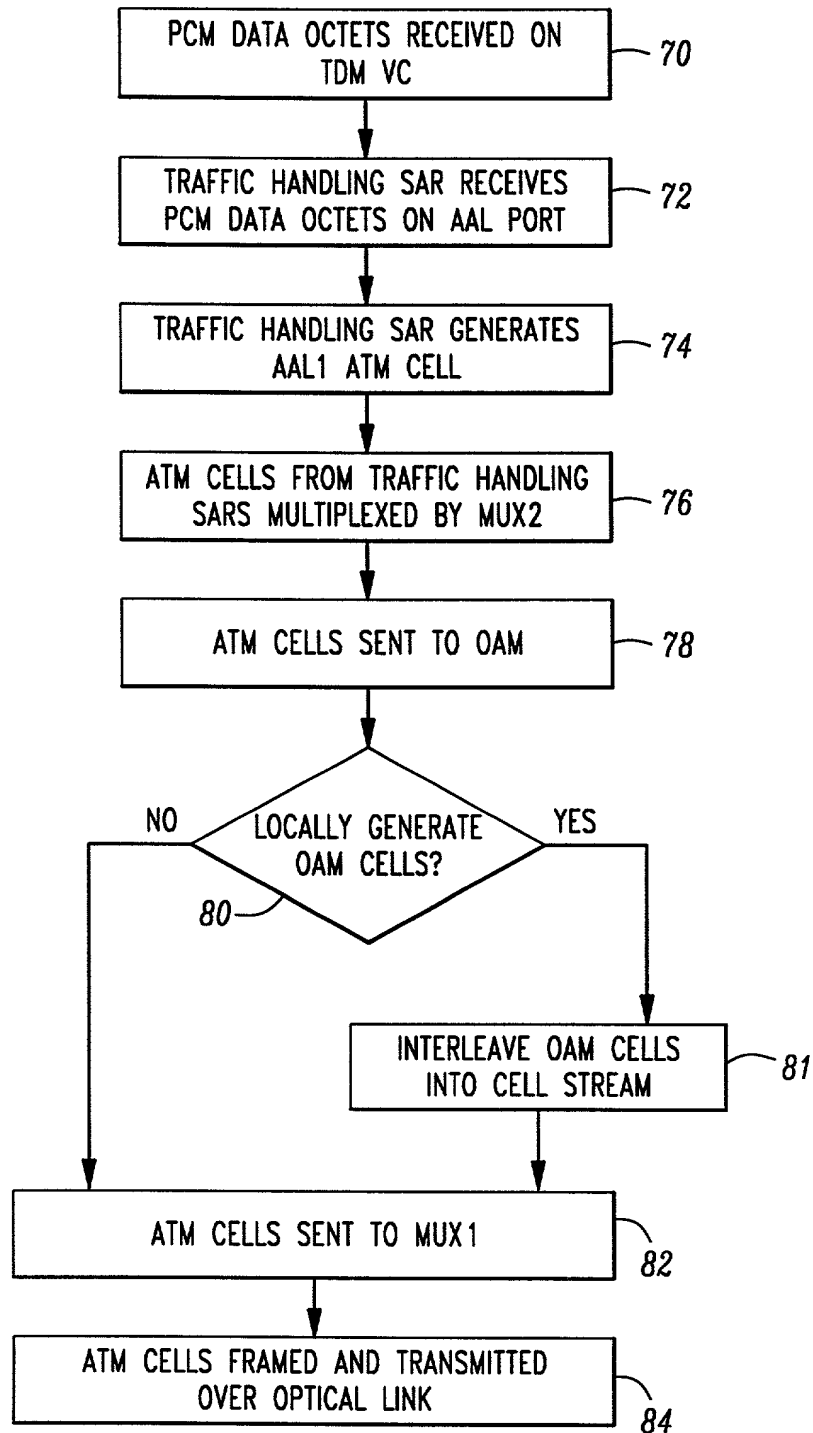
FIG. 5

FIG. 6A

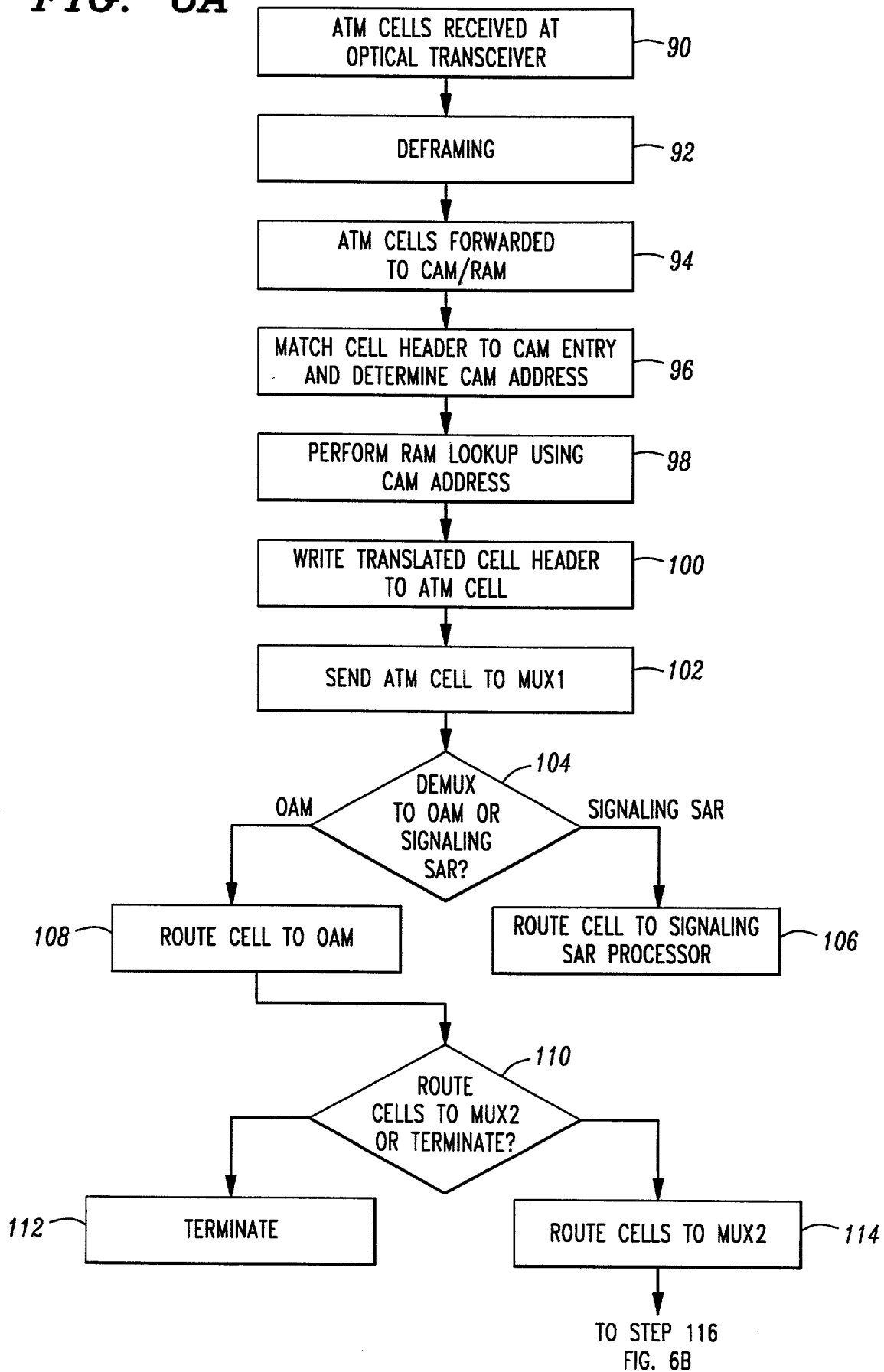


FIG. 6B

